

ABSTRACT

A method and system provides for efficient dispersal of instructions to be executed by a processor using a distributed methodology of a centralized scheduling structure. The method and system include mapping instructions received from at least
5 two instruction groups during a first stage followed by remapping, merging, and distributing instructions to a plurality of functional units during a second stage. The use of a first and second stage allowing an increased number of instructions to be executed by a processor operating at a given clock rate.